

#3

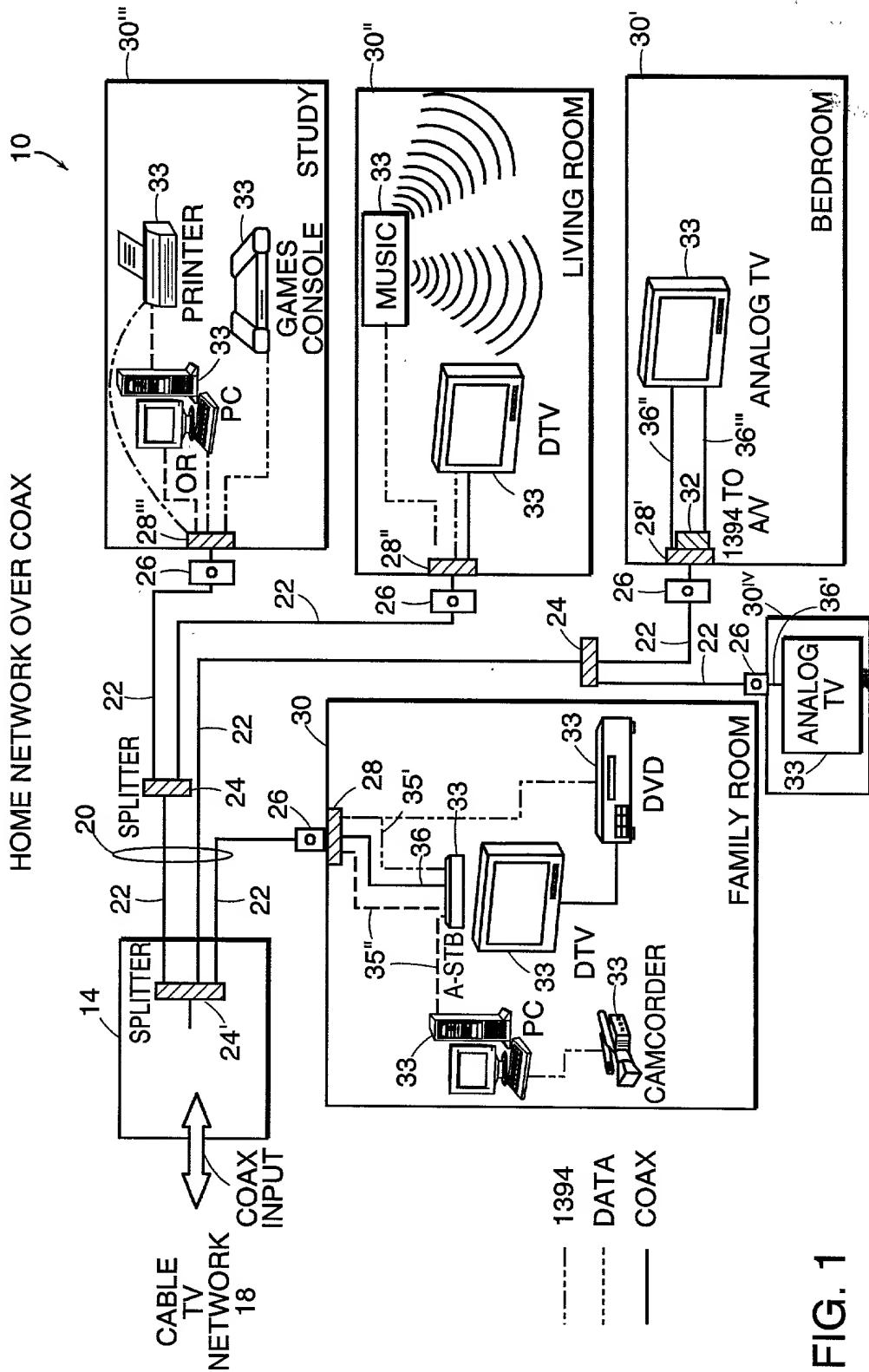


FIG.

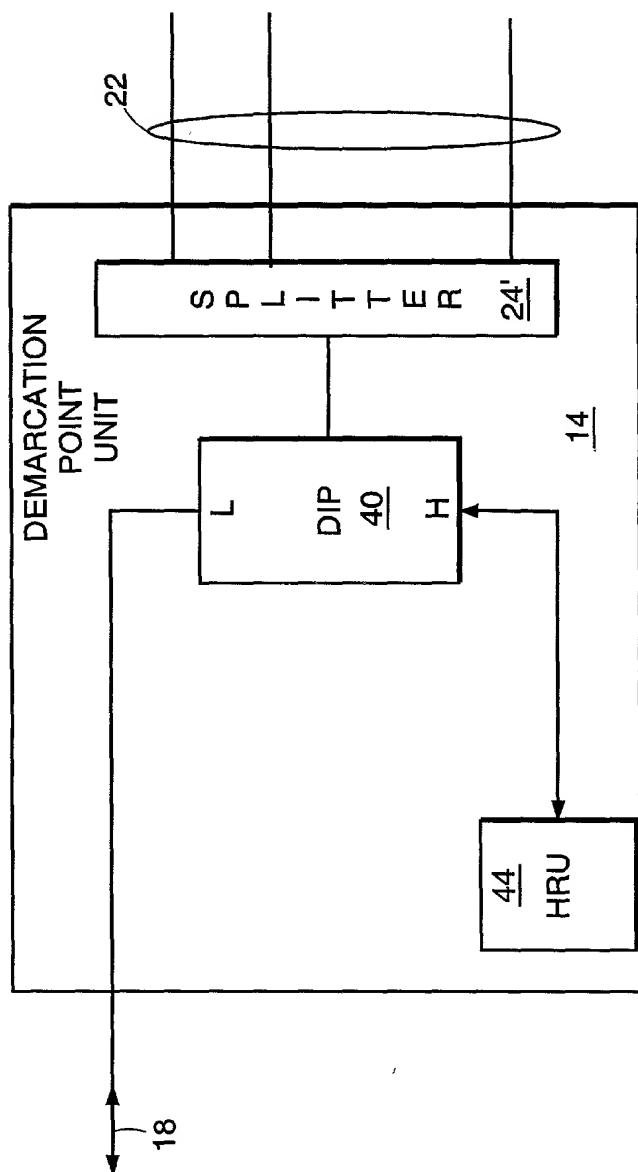


FIG. 2

PHYSICAL LAYER BLOCK DIAGRAM - REFLECTOR

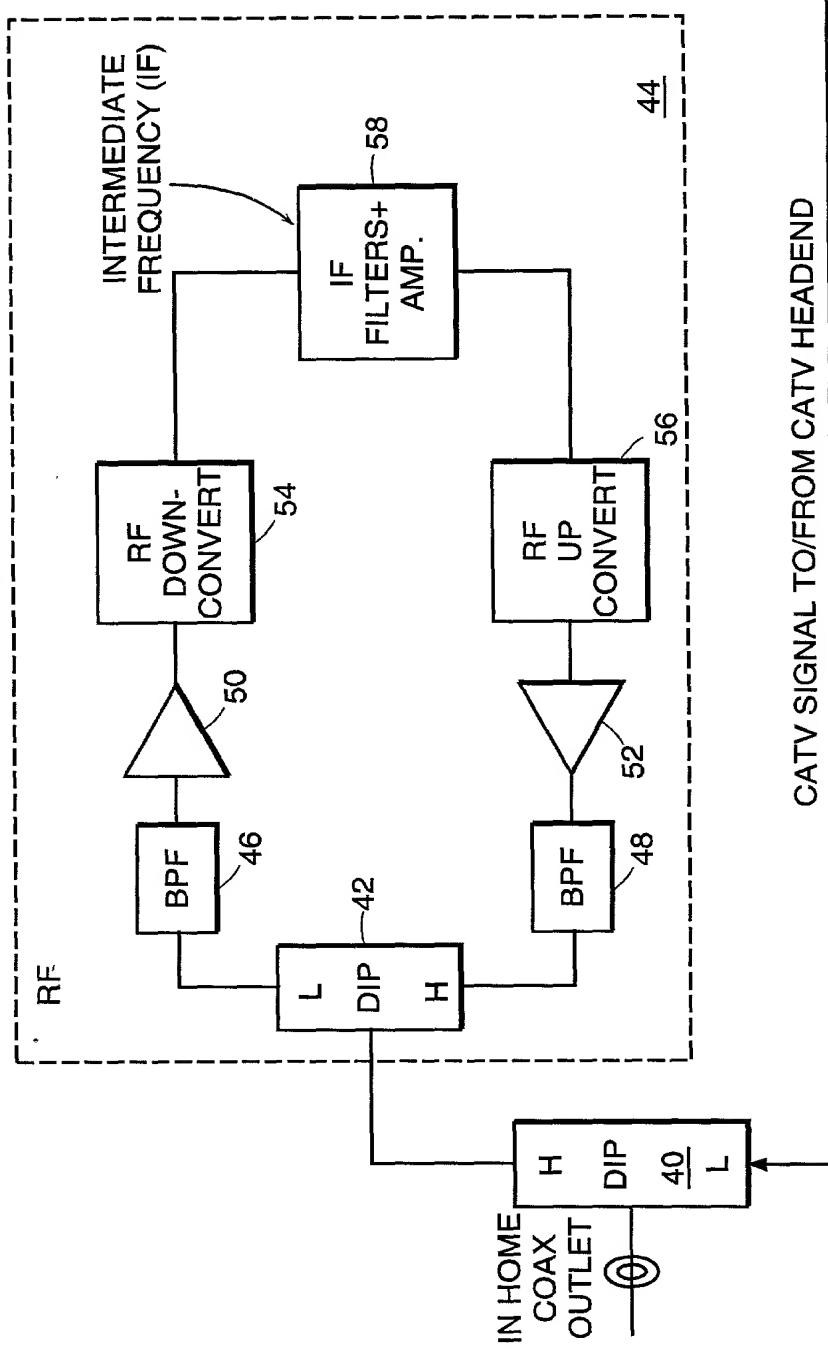
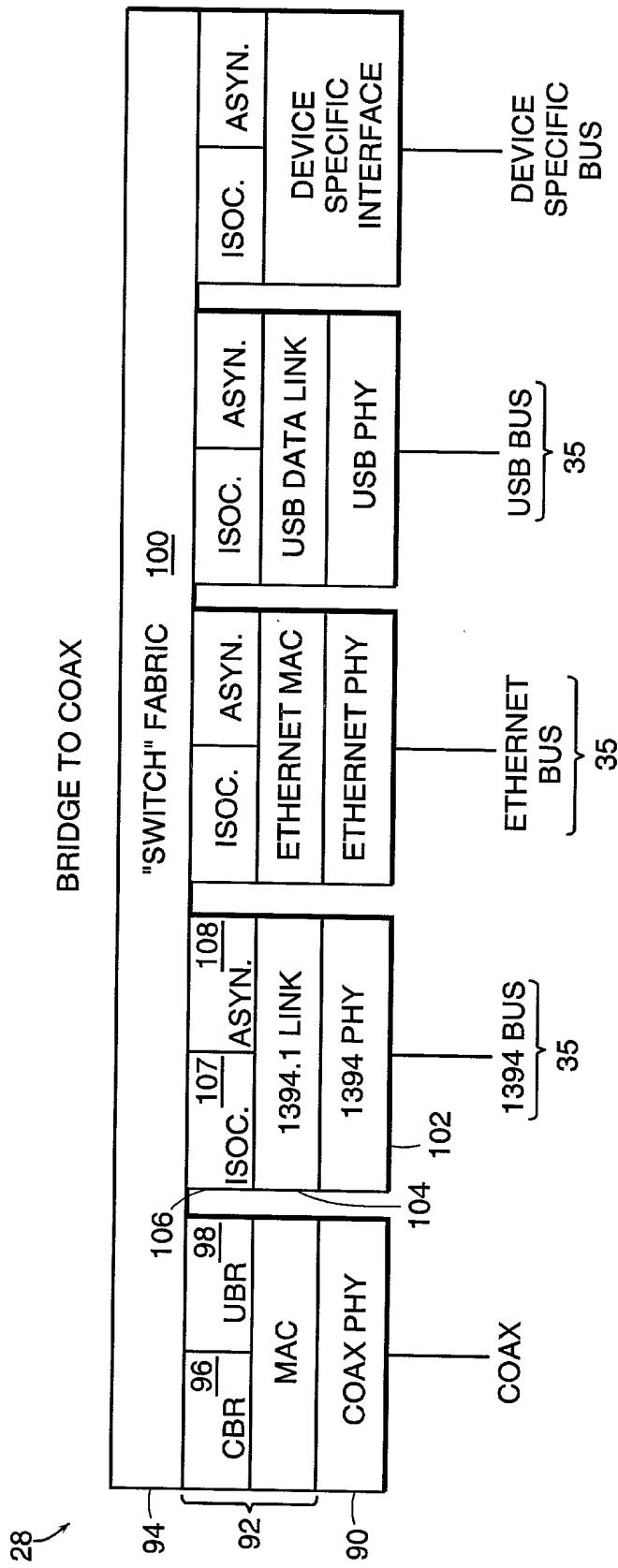


FIG. 2A



28

FIG. 3

90

PHYSICAL LAYER BLOCK DIAGRAM - HNM

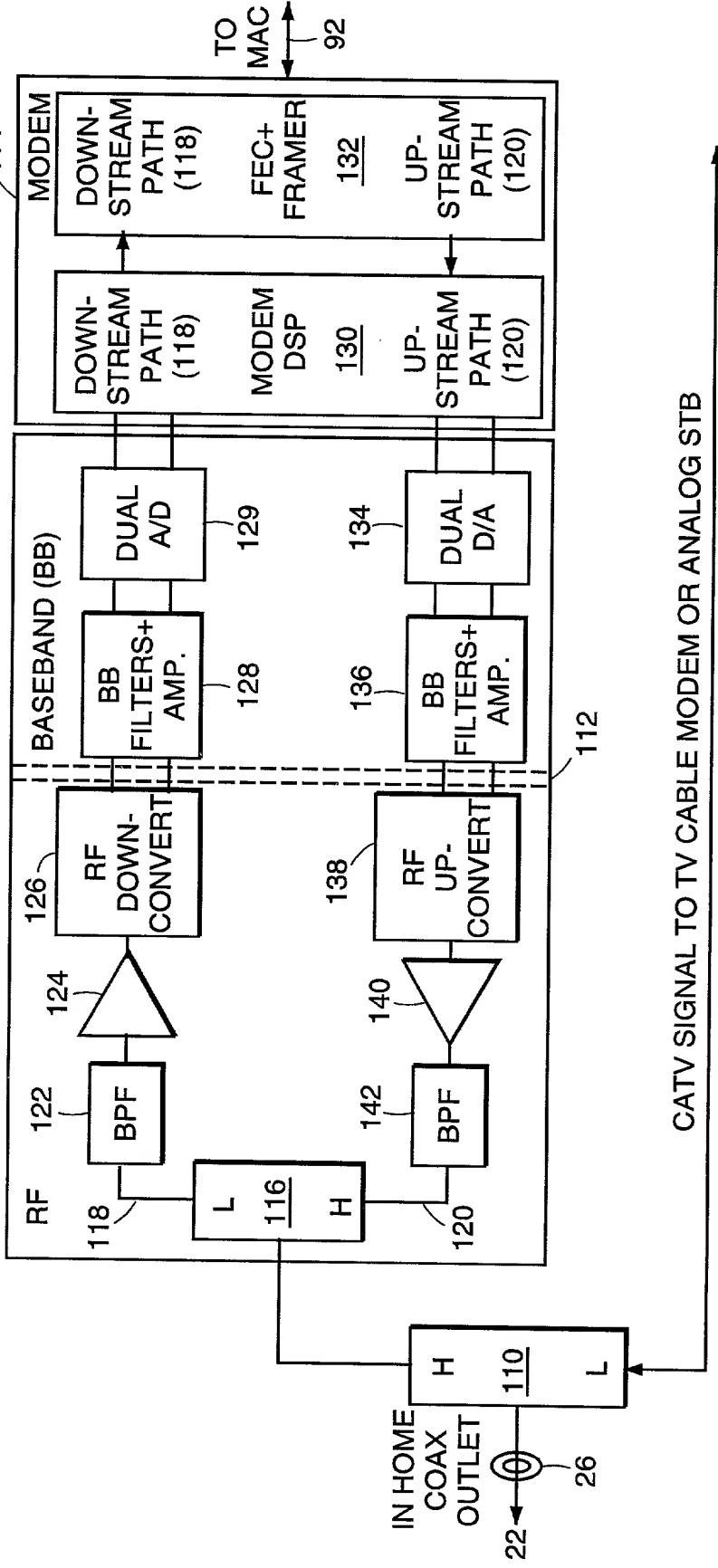
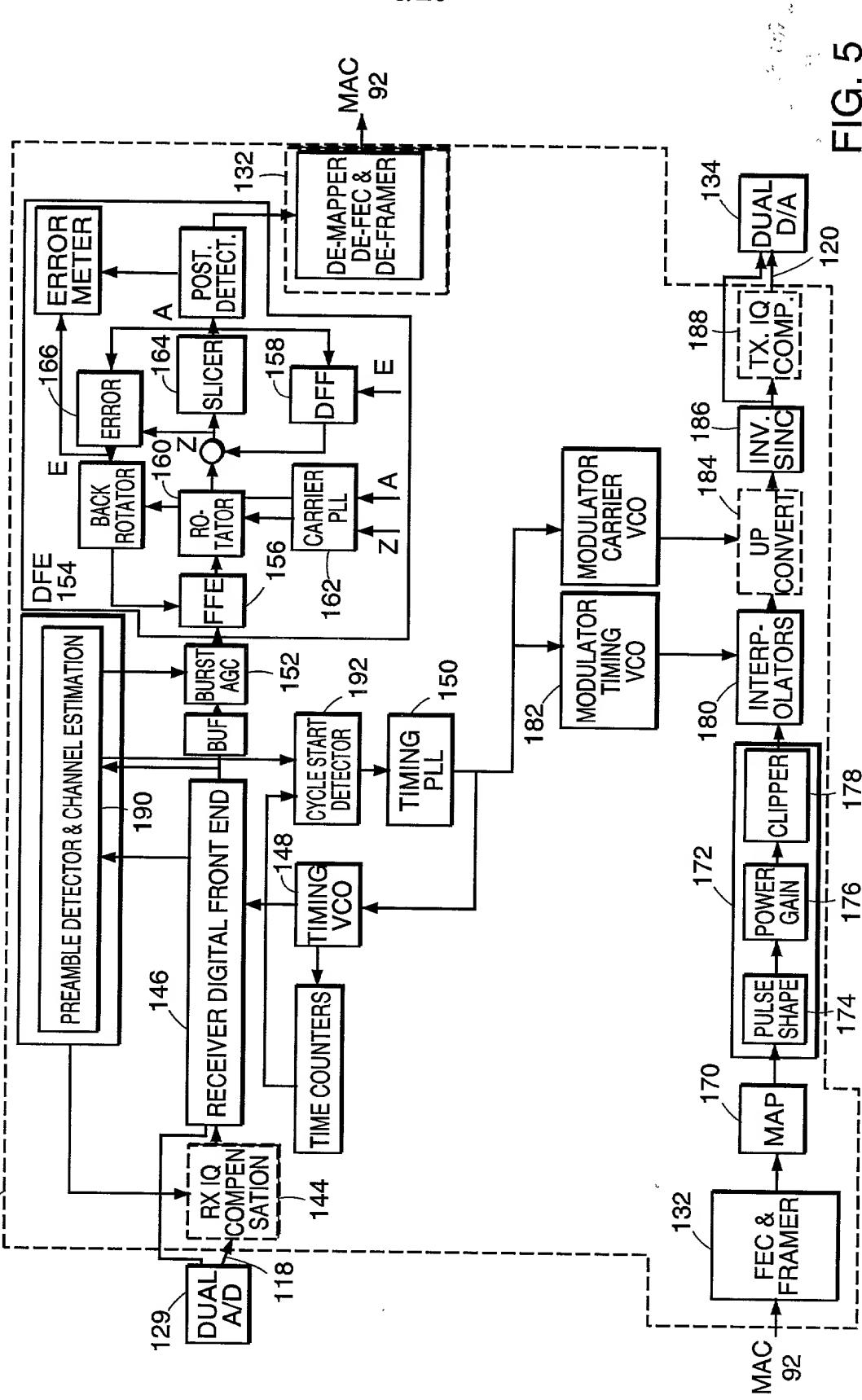


FIG. 4

CATV SIGNAL TO TV CABLE MODEM OR ANALOG STB

MODEM BLOCK DIAGRAM



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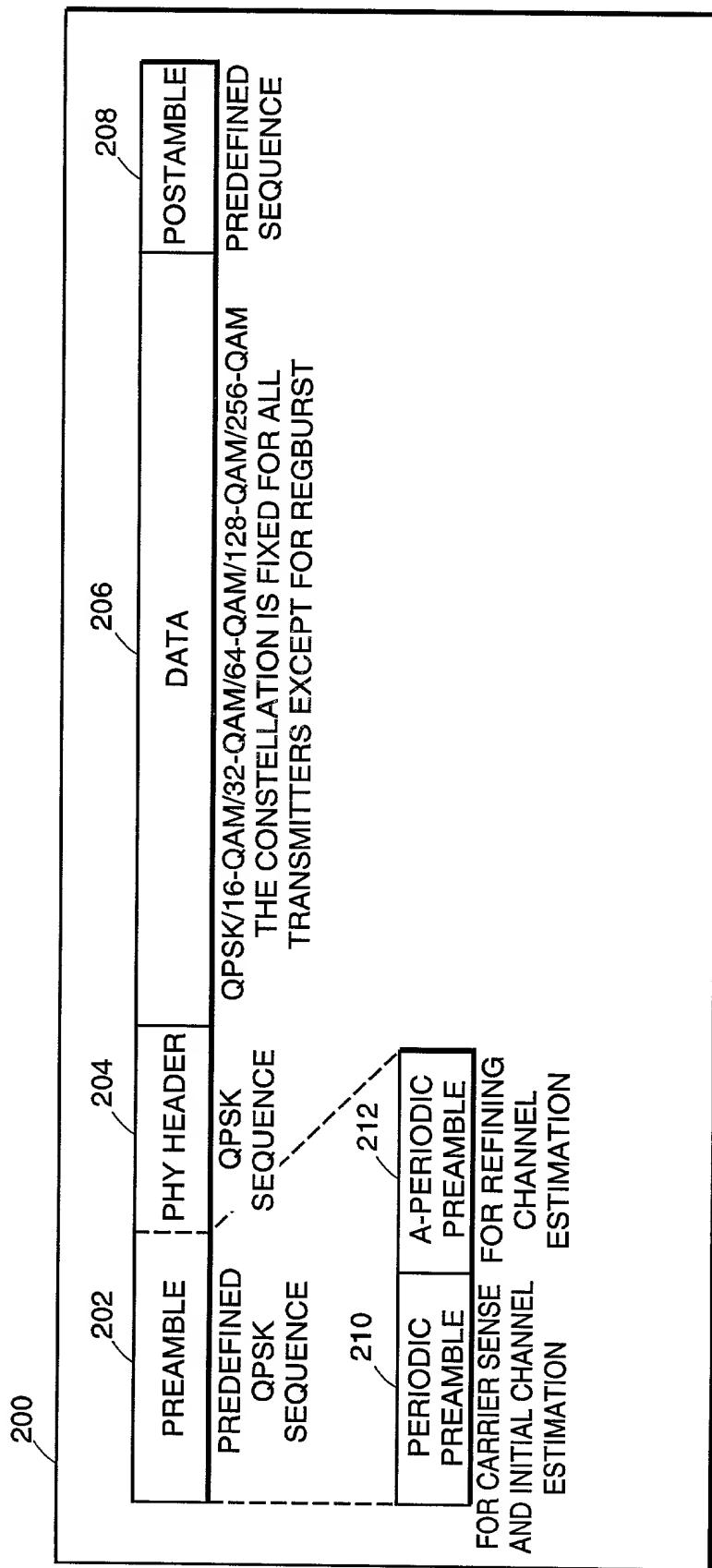


FIG. 6

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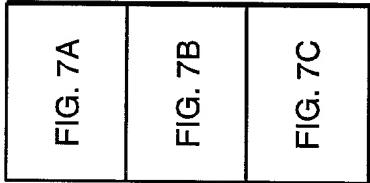


FIG. 7

EACH TRANSMISSION CYCLE STARTS WITH A
MASTER CBR BURST -CYCLE START BURST



AFTER ACK CYCLE, THE FOLLOWING MASTER
UBR BURST (CONSIDERING THE CBR, UBR
PRIORITY) MARKS THE START OF THE FAIRNESS
CYCLE WITH FAIRN_START_BURST.



AFTER REGISTRATION PERIOD, THE
FOLLOWING MASTER BURST STARTS THE
ACK CYCLE WITH ITS ACK BURST.

FIG. 7A



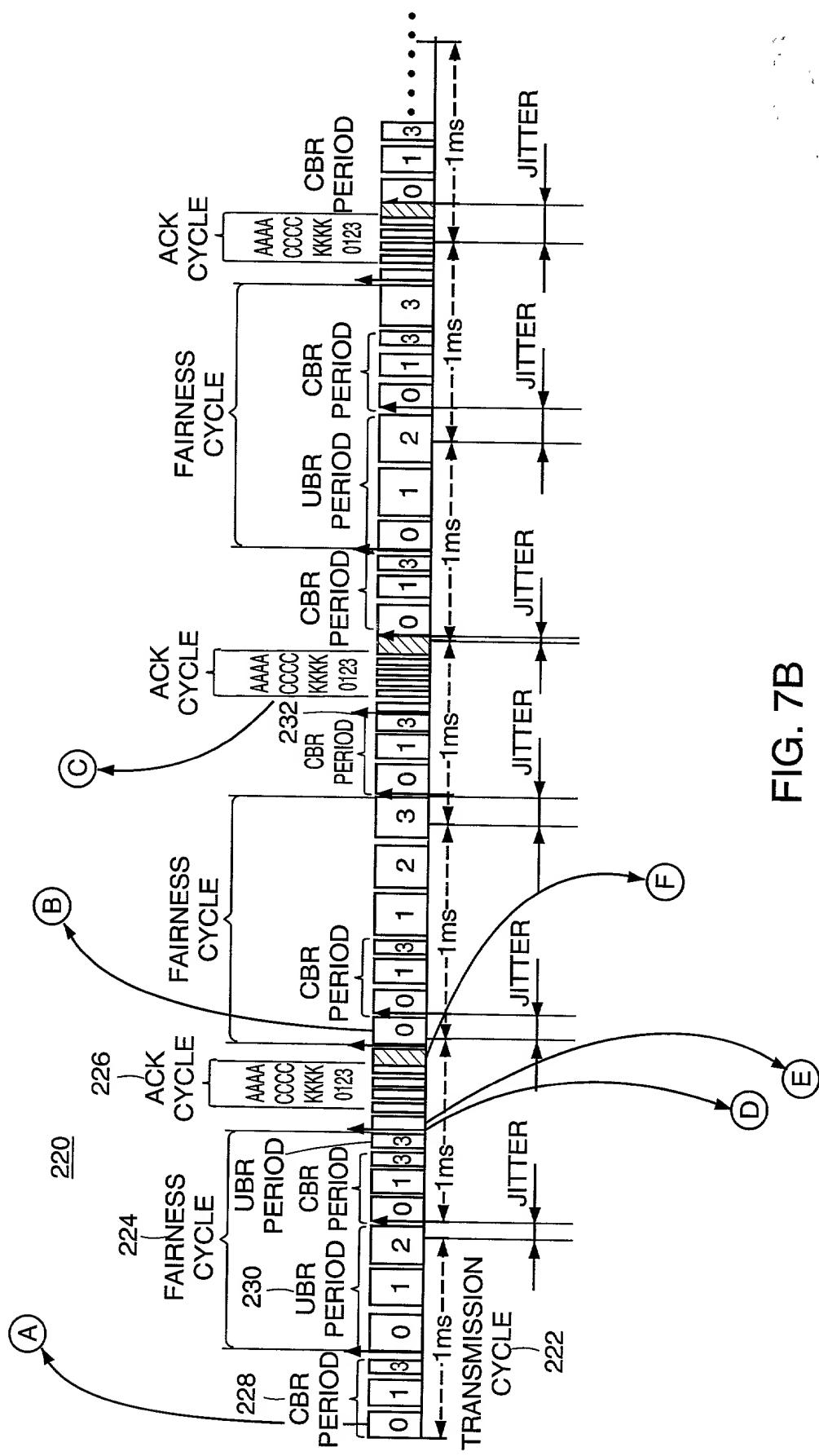


FIG. 7B

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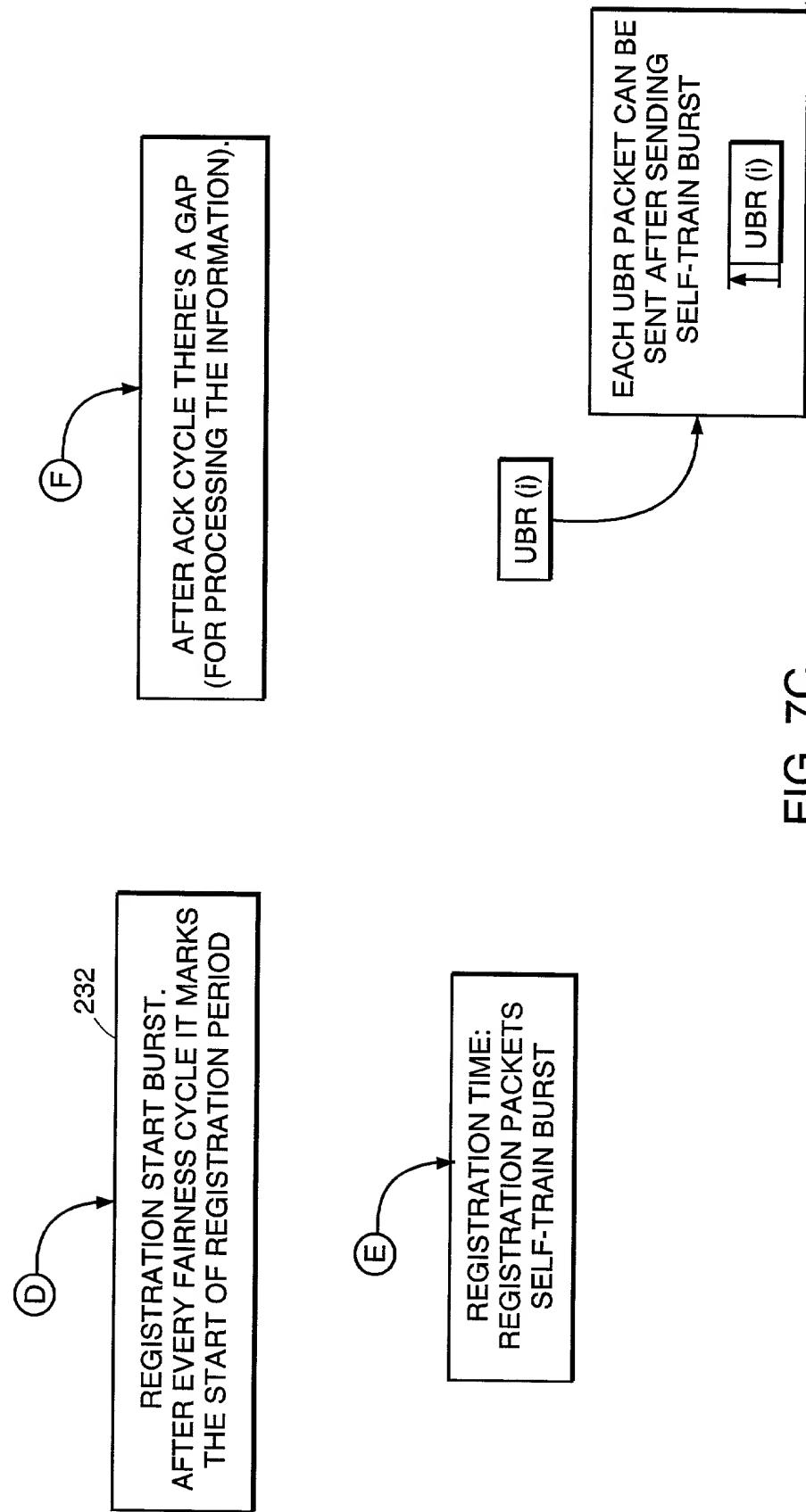


FIG. 7C

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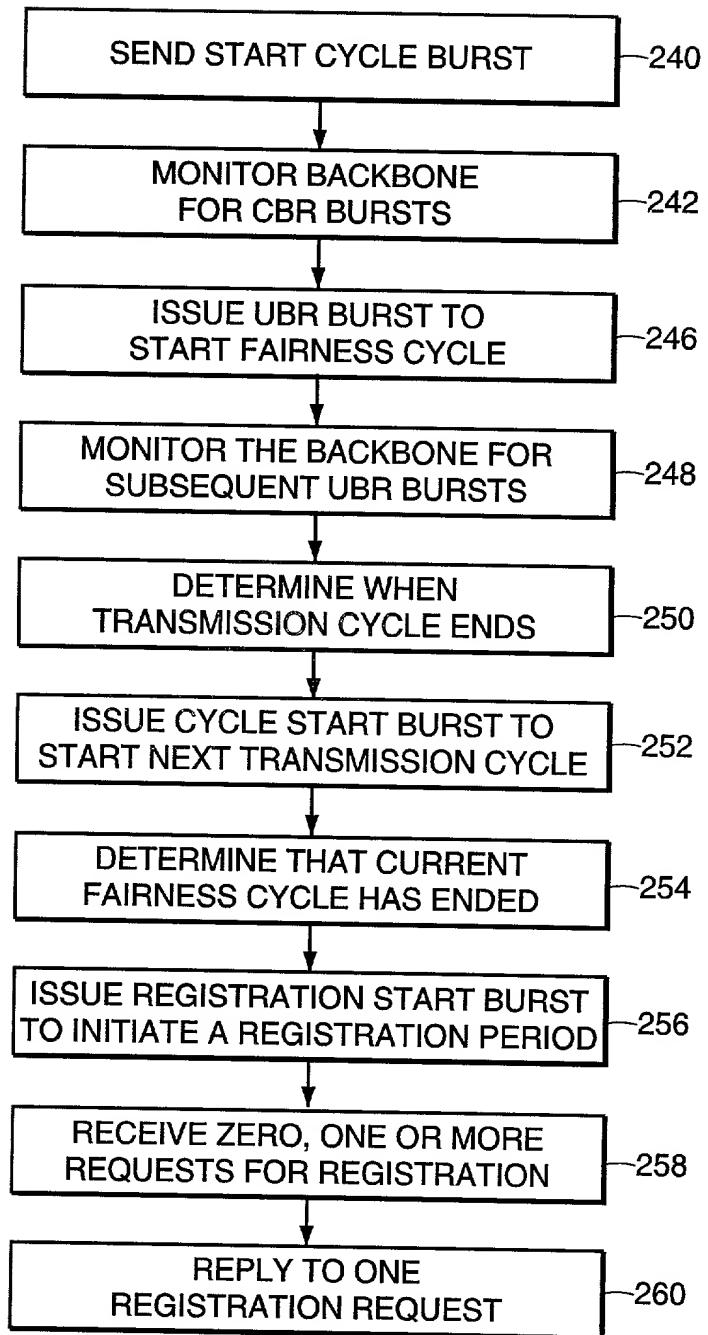


FIG. 8A

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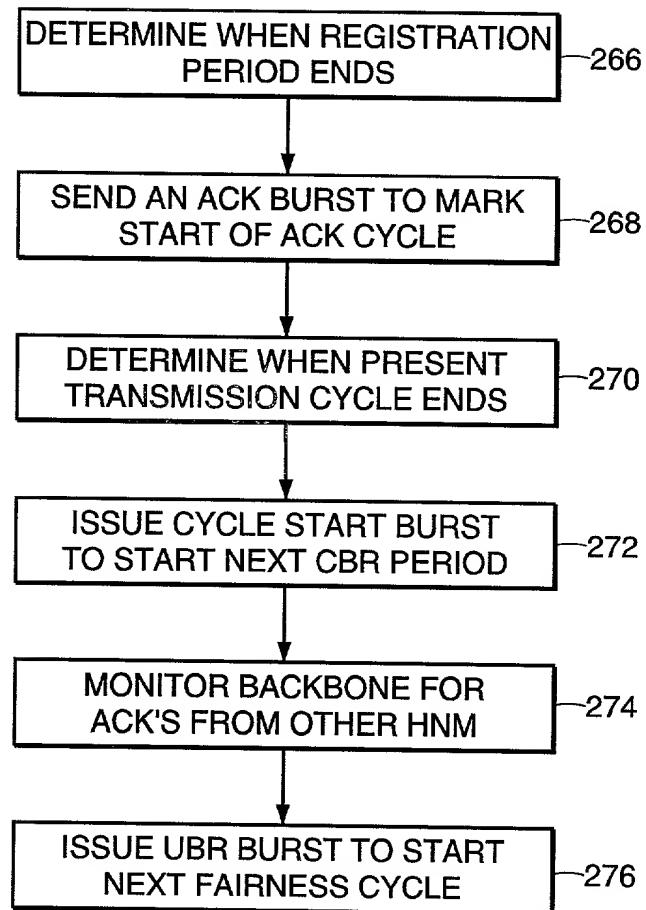


FIG. 8B

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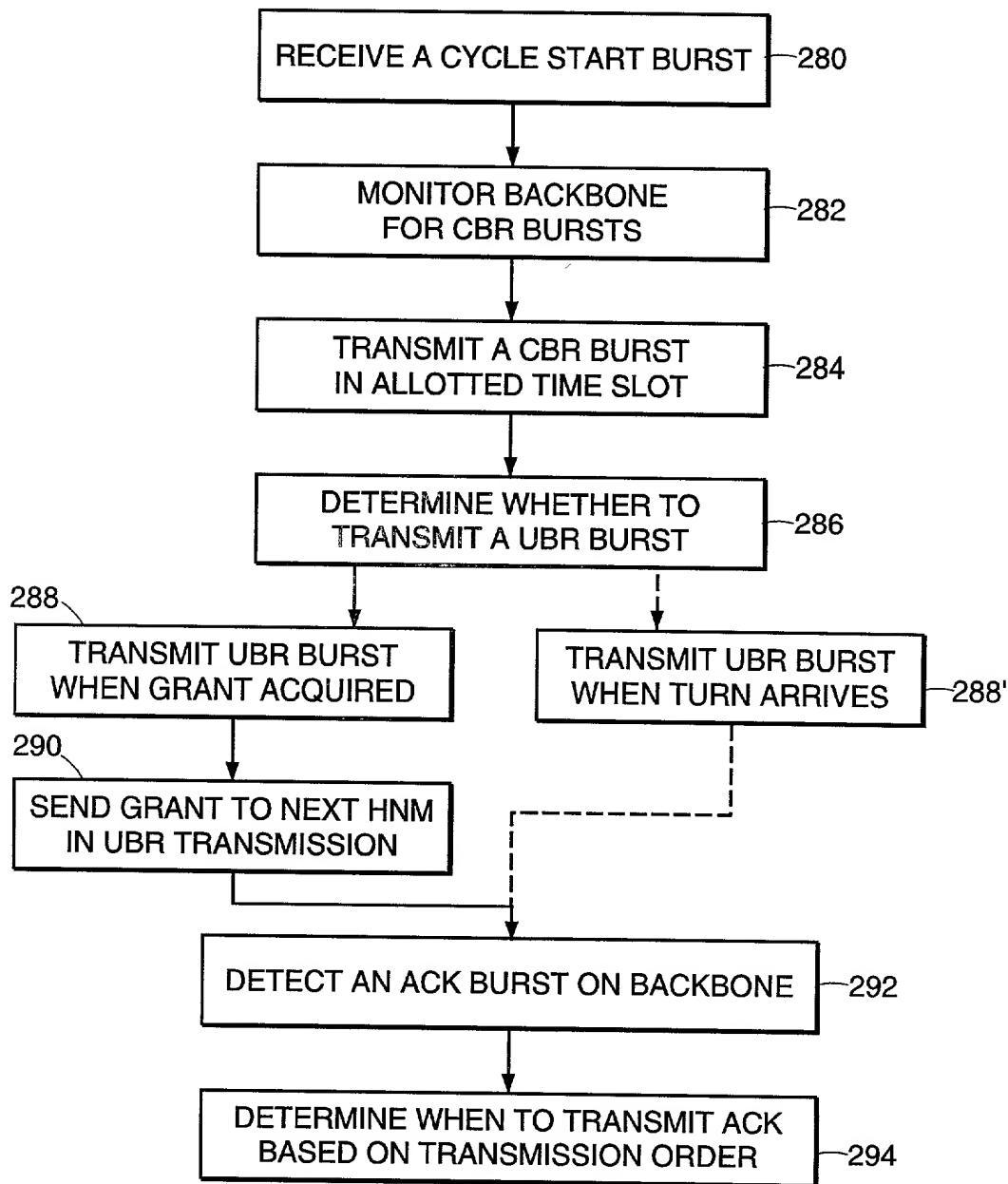


FIG. 9

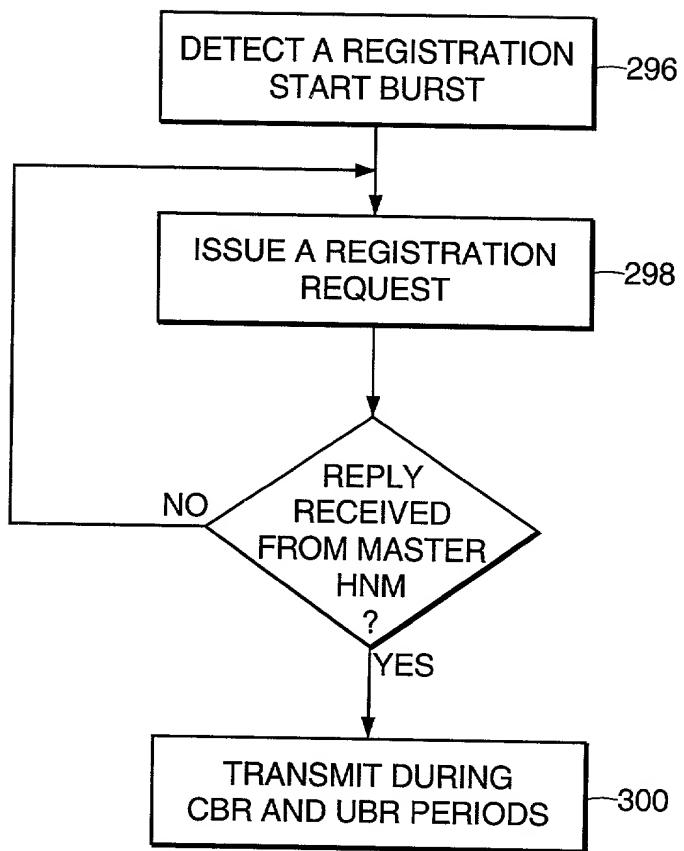


FIG. 10

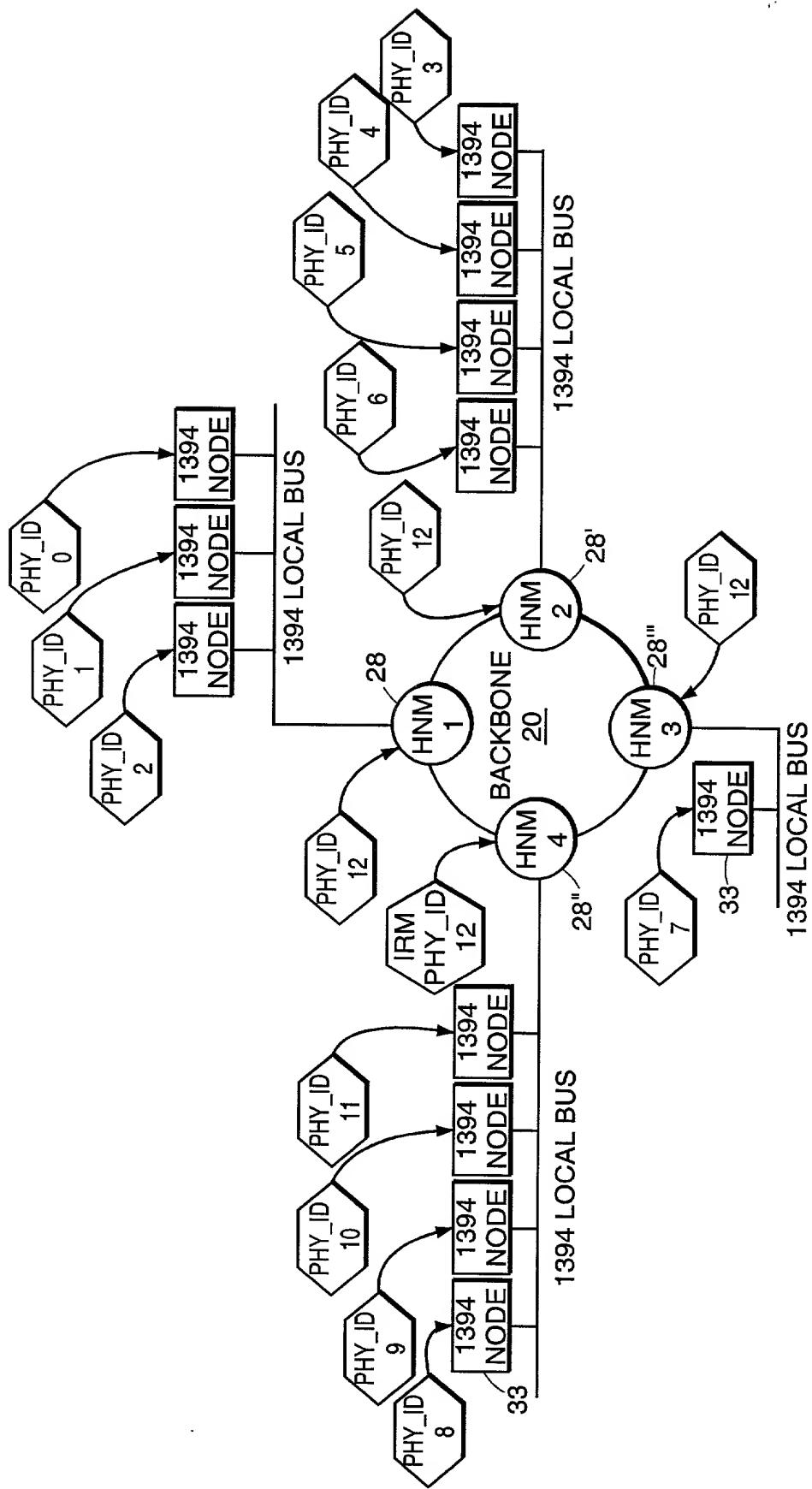
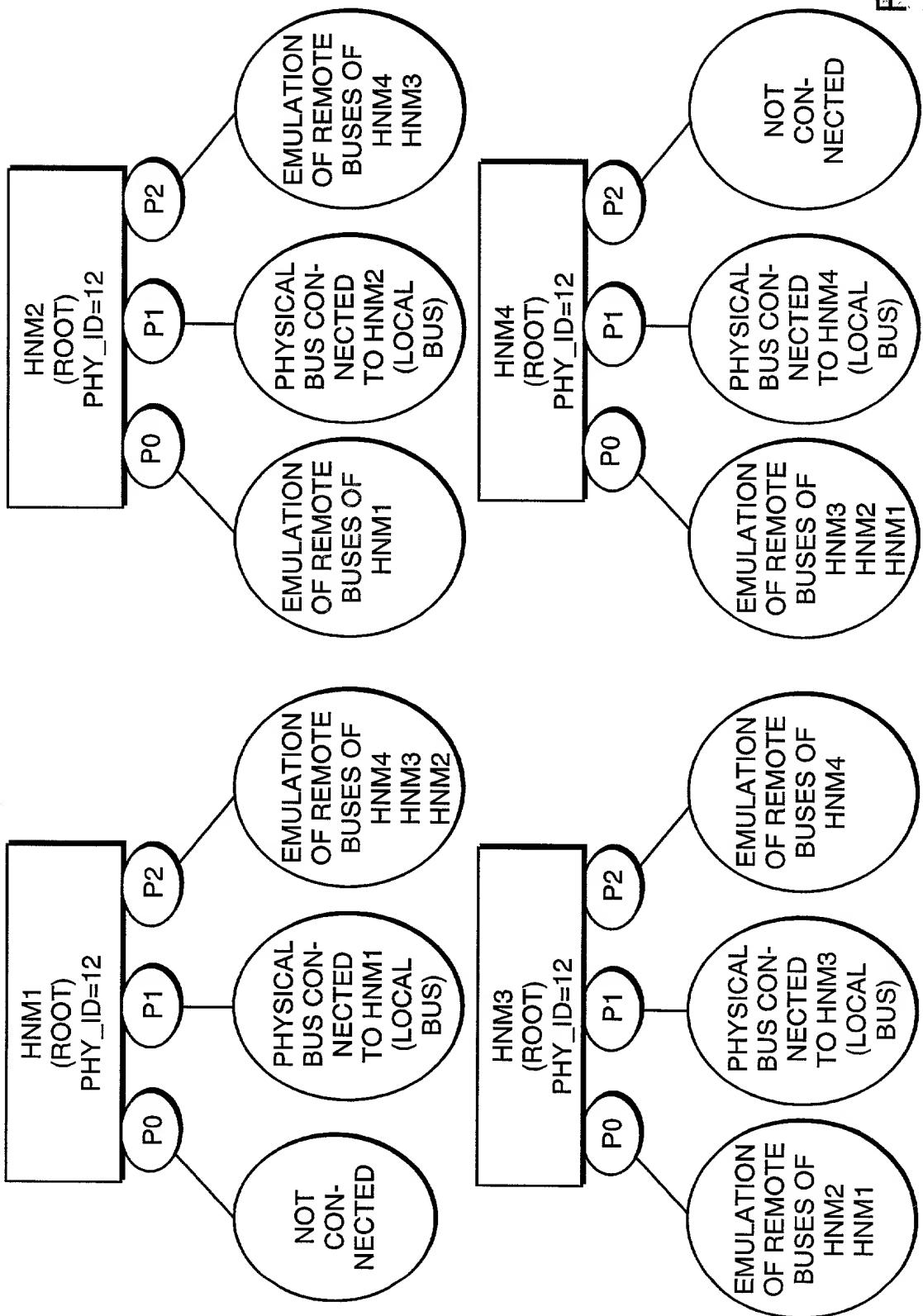


FIG. 11

FIG. 12



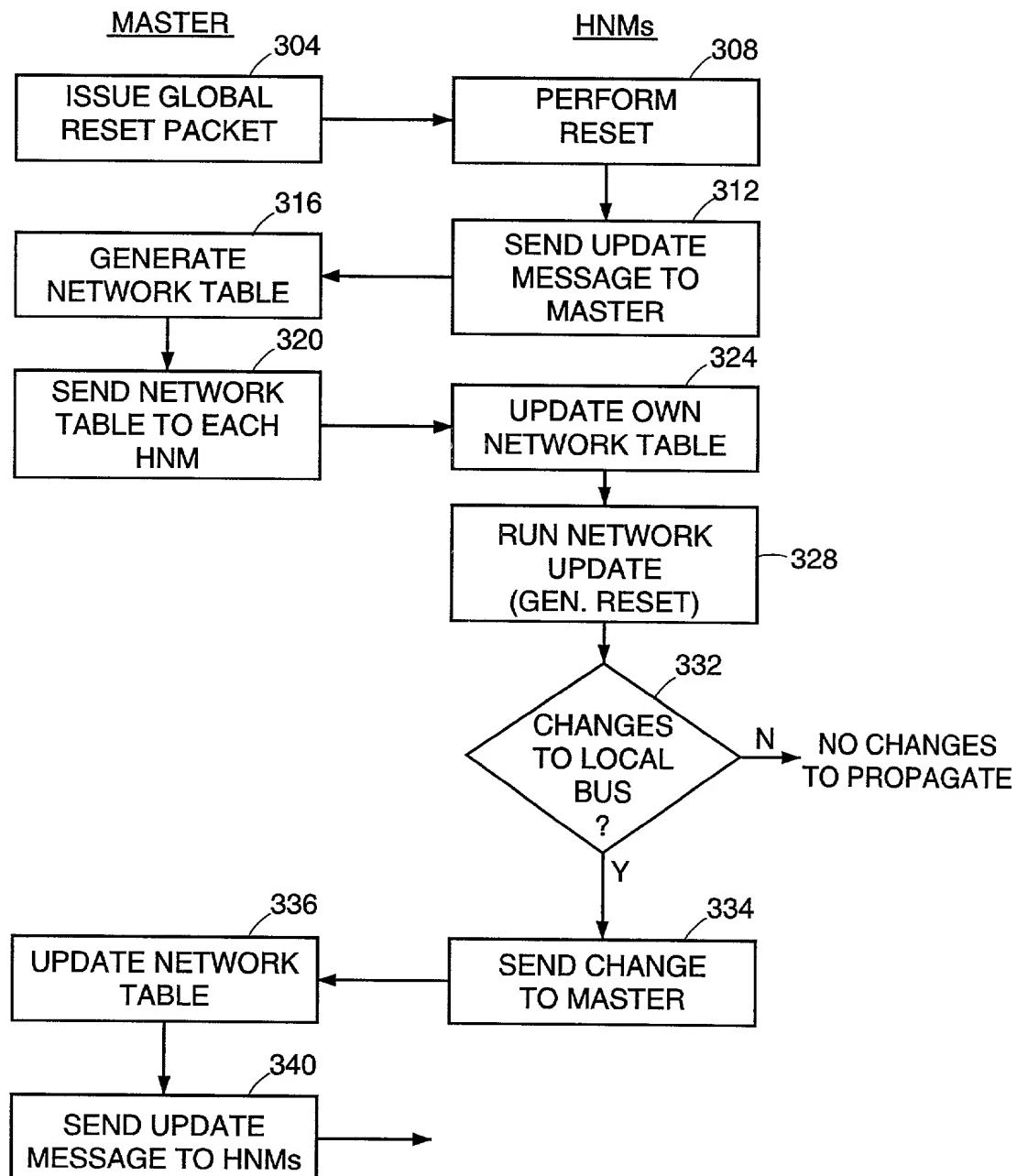


FIG. 13

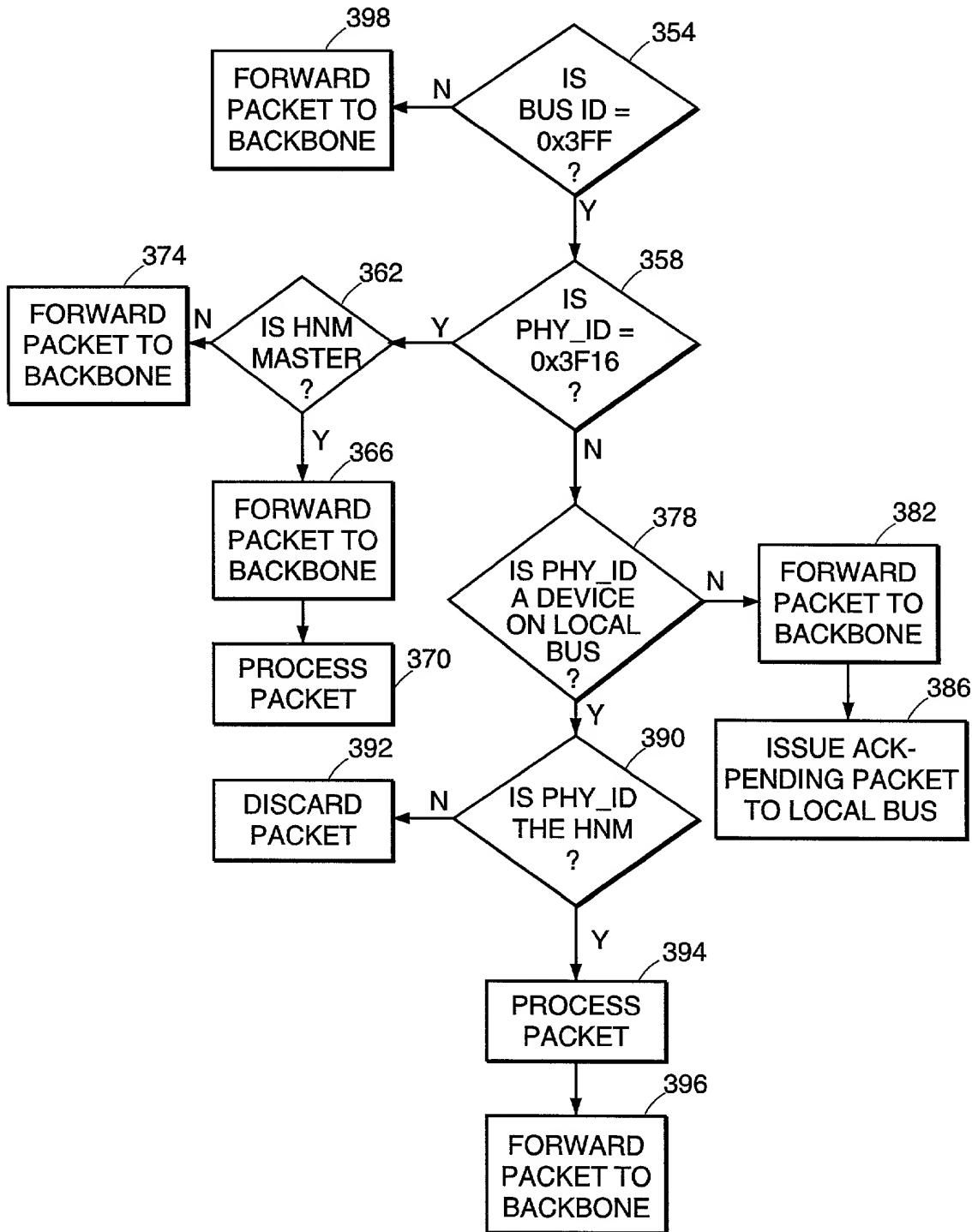


FIG. 14

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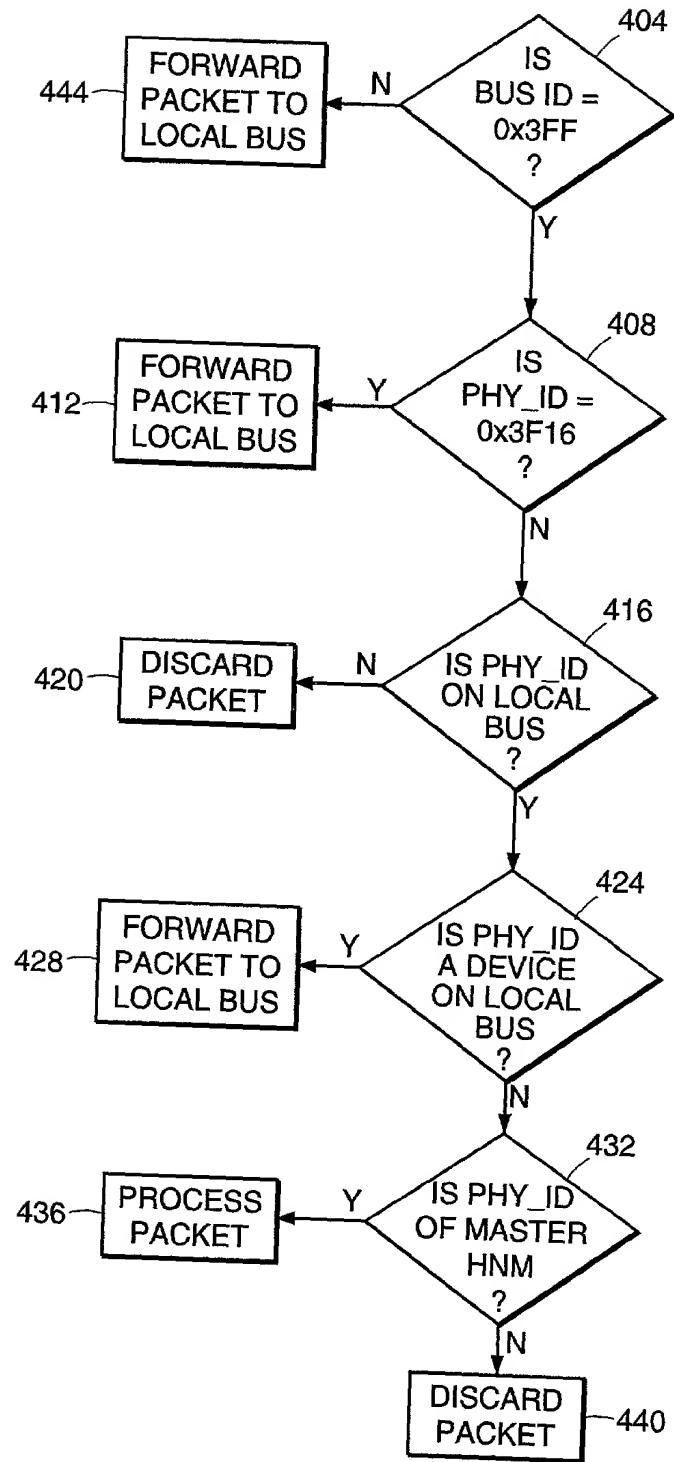


FIG. 15

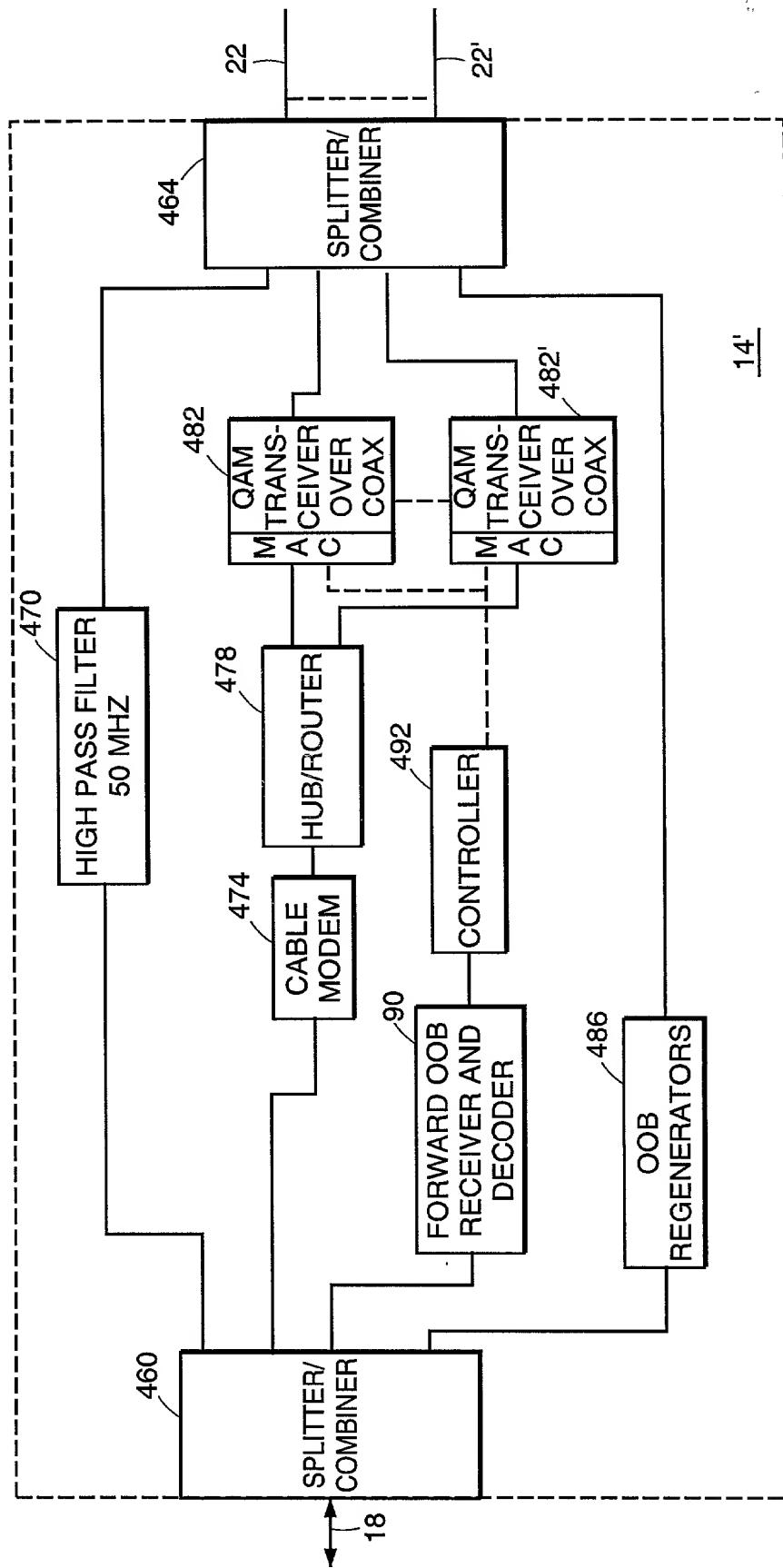


FIG. 16